

STREAMING OUTPUT PERIPHERALS FOR PROGRAMMABLE CHIP SYSTEMS

Abstract of the Disclosure

5 Methods and apparatus are provided for configuring and generating a
sequence of discrete output signal values. A microcontroller writes output signal
values to memory and provides address and timing information to a streaming output
peripheral. The streaming output peripheral uses the address and timing information
to read output signals values from memory and provide the output signal values in a
10 clock cycle accurate manner.